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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/647,929	08/26/2003	Masao Kaizuka	64272/33202	3079	
23380	7590 09/12/2006	EXAMINER			
TUCKER, ELLIS & WEST LLP 1150 HUNTINGTON BUILDING			KINKEAD, ARNOLD M		
925 EUCLID			ART UNIT	PAPER NUMBER	
CLEVELANI	O, OH 44115-1414		2817		

DATE MAILED: 09/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Арр	Application No. Applicant(s)						
		10/6	10/647,929 KAIZUKA, MASAO		0				
		Exa	miner	Art Unit					
			old M. Kinkead	2817					
 Period for	The MAILING DATE of this commun Reply	ication appears	on the cover sheet wi	th the correspondence ac	idress				
WHICH - Extensi after SI - If NO p - Failure Any rep	RTENED STATUTORY PERIOD F HEVER IS LONGER, FROM THE M ions of time may be available under the provisions X (6) MONTHS from the mailing date of this commercial for reply is specified above, the maximum stato reply within the set or extended period for reply ply received by the Office later than three months a patent term adjustment. See 37 CFR 1.704(b).	IAILING DATE (of 37 CFR 1.136(a). In nunication. atutory period will apply will, by statute, cause	OF THIS COMMUNION TO THIS COMMUNION TO THIS COMMUNION THE PROPERTY AND THE APPLICATION TO DECOME AS	CATION. eply be timely filed ITHS from the mailing date of this of the control					
Status									
1) 🗌 F	Responsive to communication(s) file	ed on							
			n is non-final						
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	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.								
		oo andor Ex pair	io Quayio, 1000 C.D	. 11, 400 0.0. 210.					
	n of Claims								
	Claim(s) 1-20 is/are pending in the application.								
	4a) Of the above claim(s) is/are withdrawn from consideration.								
	Claim(s) is/are allowed.								
	Claim(s) <u>1-20</u> is/are rejected.								
	Claim(s) is/are objected to.								
8)LJ C	Claim(s) are subject to restric	ction and/or elec	tion requirement.						
Applicatio	n Papers								
9)⊠ TI	he specification is objected to by th	e Examiner.							
10)⊠ The drawing(s) filed on <u>27 August 2003</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.									
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).									
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).									
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.									
Priority un	der 35 U.S.C. § 119								
12\□ A	cknowledgment is made of a claim	for foreign priori	ty under 35 LLS C 8	(110(a) (d) or (f)					
	12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:								
_	1. Certified copies of the priority documents have been received.								
	2. Certified copies of the priority documents have been received in Application No								
	3. Copies of the certified copies of the priority documents have been received in this National Stage								
·	application from the International Bureau (PCT Rule 17.2(a)).								
* Se	* See the attached detailed Office action for a list of the certified copies not received.								
occurs attached detailed Office action for a list of the certified copies not received.									
Attachment(s	3)								
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)									
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date Notice of Informal Patent Application (PTO-152)									
Paper No(s)/Mail Date 6) Other: <u>See Continuation Sheet.</u>									

Continuation of Attachment(s) 6). Other: data tables difficult to read please resubmit.

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DETAILED ACTION

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Specification

 The disclosure is objected to because of the following informalities: The data tables provided are difficult to read; please resubmit in larger print.

Appropriate correction is required.

Double Patenting

1. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. A nonstatutory obviousness-type double patenting rejection is appropriate where the conflicting claims are not identical, but at least one examined application claim is not patentably distinct from the reference claim(s) because the examined application claim is either anticipated by, or would have been obvious over, the reference claim(s). See, e.g., *In re Berg*, 140 F.3d 1428, 46 USPQ2d 1226 (Fed. Cir. 1998); *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) or 1.321(d) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent either is shown to be commonly owned with this application, or claims an invention made as a result of activities undertaken within the scope of a joint research agreement.

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

2. Claims 1-20 are provisionally rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claims 1-20 of copending Application No. 10/770,643. Although the conflicting claims are not identical, they are not patentably distinct from each other because the claims presented in this application are merely narrower in scope with regards the frequency modulated spread spectrum clock generator apparatus and method steps. More, specifically, the value for N>1 has been provided, see claim 4 of this application(* 929), where

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N=4; this is simply selecting N>1 as claimed in the other (' 643)application and would be obvious to one of ordinary skill in the art.

This is a <u>provisional</u> obviousness-type double patenting rejection because the conflicting claims have not in fact been patented.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

4. Claims 1-3,15 and 16 are rejected under 35 U.S.C. 102(e) as being anticipated by Lee et al (US 6,993,109).

The reference by Lee et al discloses a clock circuit and method for reducing EMI, see abstract, and figure 9,15, and

16, where a frequency modulated clock generator is shown in figure 9, with clock input(iCLK), a digital delay(95,97)

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coupled to the CLk input, controller 93 providing the data input to the delay input stage(95), and clock output(76); the

numeric sequencer is shown by figures 15 and 16, which together allow for the proper sequence/selection of delay

via the controller as the delay data. A binary counter(133 is shown in figure 16, the mux being used as the pattern

generator to select the correct delay. The method steps being inherent.

Any inquiry concerning this communication or earlier communications from the examiner should be directed

to Arnold M. Kinkead whose telephone number is 571-272-1763. The examiner can normally be reached on Mon-Fri,

8:30 am -5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal

can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is

assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information

Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or

Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more

information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the

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assistance from a USPTO Customer Service Representative or access to the automated information system, call

800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Primary Examiner

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